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UFV

Accelerating the exact evaluation of geometric predicates with GPUs

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Our Team

- Marcelo de Matos Menezes, UFV: master's student since 2018; interests are Computer Graphics, Computational Geometry and High-Performance Computing, long-term goal to apply the ideas described on this paper to other CG algorithms.
- Salles Viana Gomes Magalhães, RPI PhD grad, UFV prof
- W. Randolph Franklin, RPI prof.
- Matheus Aguilar de Oliveira, UFV: CS undergrad since 2018; interests are Computational Geometry and Competitive Programming.
- Rodrigo E. O. Bauer Chichorro, UFV: CS undergrad since 2017; interests are Competitive Programming, Computational Geometry and Artificial Intelligence











Our research strategy

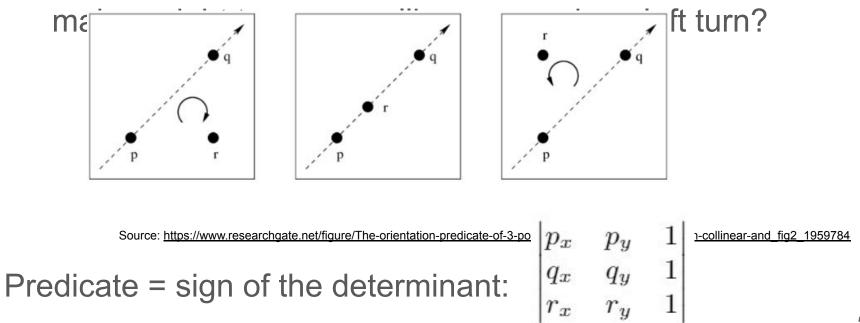
- Identify fundamental geometric operations
- used in higher-level systems
- that need to produce correct results.
- and should execute very fast.
- Devise new theory
- using simple data structures
- on current hardware.
- Implement
- Test.

This paper's contribution

- A faster solution to erroneous computations caused by floating point finite precision computations.
- Errors can cause predicates (conditionals)to be evaluated wrong.
- That can cause topological errors.
- Existing solutions are either very slow or may fail.
- We synergize three software techniques and two hardware platforms.
 - filter input with uniform grid on CPU, then
 - filter survivors with interval arithmetic on GPU, finally
 - if necessary, compute exactly with multiprecision rationals back on CPU.
- Result: both fast and good.

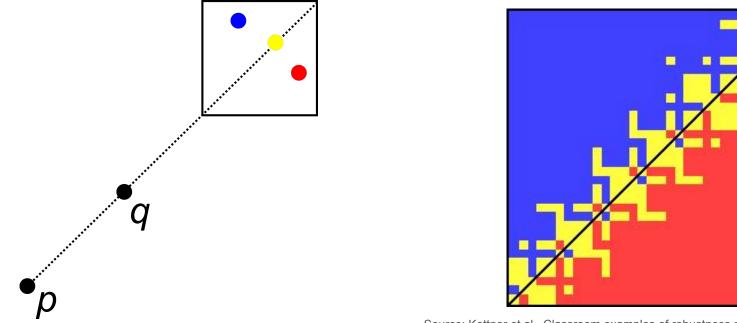
The problem of roundoff errors

- Floating-point errors: computational geometry challenge.
- Generate topological inconsistencies: global impossibilities.
 - intersection point between two lines may not lie in either.
- Example: planar orientation predicate
 - Do three points p = (px, py), q = (qx, qy) and r = (rx, ry)



Roundoff errors

• Evaluating the predicate using floating point arithmetic:



Source: Kettner et al., Classroom examples of robustness problems in geometric computations

 Common techniques (snap rounding, epsilon tweaking, etc): no guarantee

Rationals: one roundoff error solution

- Solution for roundoff errors: exact arithmetic (e.g. GMP rationals), but challenges:
 - Slower than floats
 - Size is exponential in depth of computation tree, although that's not a problem if the tree is shallow
 - Growing the size of a variable allocates memory on the global heap.
 - Total time may be superlinear in the number of objects, and
 - is serial,
- Apparently little prior art of working (not just proposed) rational number systems on GPUs

Arithmetic filters and Interval arithmetic (IA), 1

- Technique used in several CG implementations, e.g.: CGAL
- Basic idea: use exact arithmetic only when really necessary
- Predicate evaluation: typically "=" sign of an arithmetic expression
- Each value has:
 - an exact value (can be lazily computed), and
 - an approximation given by an interval [xl, xh].
- Predicates evaluated using the approximation
- If the sign of the exact result can be <u>safely</u> inferred based on results computed with the intervals \rightarrow use that sign
- Otherwise (a.k.a. <u>filter failure</u>) → re-evaluate with exact arithmetic

Arithmetic filters and IA, 2

- IA used to compute the sign of an expression.
- If it reports a *non-zero* result, it's guaranteed to be correct.
- Sometimes it reports a failure. (Then we escalate.)
- Real x represented as [x, \overline{x}], where $\underline{x} \leq x \leq \overline{x}$

$$\begin{split} & [x] + [y] &= [\underline{x} + \underline{y}, \overline{x} + \overline{y}] \\ & [x] - [y] &= [\underline{x} - \overline{y}, \overline{x} - \underline{y}] \\ & [x] \cdot [y] &= [\min\{\underline{x}\underline{y}, \underline{x}\overline{y}, \overline{x}\underline{y}, \overline{x}\overline{y}, \overline{x}\overline{y}\}, \max\{\underline{x}\underline{y}, \underline{x}\overline{y}, \overline{x}\underline{y}, \overline{x}\overline{y}, \overline{x}\overline{y}\}] \\ & [x]/[y] &= \begin{cases} [x] \cdot [1/\overline{y}, 1/\underline{y}] & \text{if } 0 \notin [y], \\ \mathbb{R} & \text{otherwise} \end{cases} \\ & [x]^{1/2} &= \begin{cases} [\underline{x}^{1/2}, \overline{x}^{1/2}] &, & \text{if } 0 \leq \underline{x} \\ \mathbb{R} &, & \text{otherwise} \end{cases} \end{split}$$

Source of the fig: Brönnimann, H., Burnikel, C., & Pion, S. (2001). Interval arithmetic yields efficient dynamic filters for computational geometry. *Discrete Applied Mathematics*, *109*(1-2), 25-47.

Arithmetic filters and IA, 3

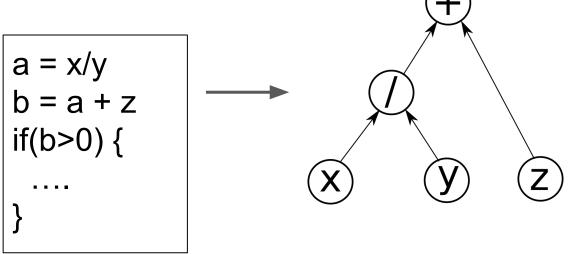
- An interval is a pair of floating-point numbers.
- To satisfy the containment property, the operations must change the way floating point values are rounded.
- IEEE-754 standard:
 - Result = exact result rounded to the next (or previous) representable FP number
 - Can be rounded:
 - towards -∞
 - to the closest FP (default)
 - towards +∞
- Changing the rounding mode on a GPU is very fast (slow on CPU)

Arithmetic filters and Interval arithmetic, 4

- CGAL uses arithmetic filters/IA- transparent to programmer but not thread safe.
- Illustration of a predicate one could implement: // Predicate: returns true if the sum of x_exact with y_exact is positive // and false otherwise. x_interval and y_interval must contain, // respectively, x_exact and y_exact.

Arithmetic filters and Interval arithmetic, 5

- CGAL: arithmetic filtering can be performed "dynamically/automatically"
- Example:
 - A DAG may be created to keep track of results
 - If exact evaluation necessary \rightarrow lazily re-evaluate the values



Exact fast parallel intersection of large 3-D triangular meshes

- Earlier work, presented last year
- Salles Magalhaes thesis
- Intersected 3D meshes using shared-memory multi-core CPUs. Combined:
 - Simulation of Simplicity
 - Arithmetic filtering/IA. "Manually" managed.
 - Parallel on multicore Intel Xeon with OpenMP
 - Big rationals.
- Today: start to incorporate GPUs.

Idea for using exact computation and GPUs

- GPUs:
 - excellent for floating-point arithmetic
 - however, warps of 32 threads should run same instruction stream on adjacent data
 - trees, hierarchical data structures, pointers are very inefficient.
- Implement the IA computation on the GPU
- CPU batch offloads evaluation of predicates to GPU.
- Indeterminate results are filtered and re-evaluated on the CPU.

What is CUDA?

- To program Nvidia GPUs.
- C++ with small syntax extensions and library.
- nvcc compiler separates program into code for CPU host and code for GPU device.
- GPU architecture is complicated.
 - thousands of cores, each 1/20 as powerful as Xeon core
 - SIMT, 32 thread warp
 - \circ several memory classes:
 - varying speed,
 - size (to 48GB),
 - latency,
 - unified VM with host.
- A range of higher level abstract layers like Thrust and Kokkos trade off programmer time and execution time.

Implementation details, 1

- Created a class, based on Collange et al., to perform the necessary calculations → easier usage
- The rounding modes on CUDA C are selected via compiler intrinsics:
 - e.g.: For addition:
 - dadd_rd() switches the rounding mode towards ∞
 - __dadd_ru() switches the rounding mode towards +∞
- These are hidden from the user through operator overloading

Implementation details, 2

```
Some methods in our CudaInterval class
    class CudaInterval {
 1
 2
    public:
 3
         __device__ __host__ CudaInterval(const double 1, const double u)
              : lb(1), ub(u) \{\}
 4
 5
          . . .
 6
         __device__ CudaInterval operator+(const CudaInterval& v) const {
              return CudaInterval(__dadd_rd(this->lb, v.lb),
 7
                   \_ dadd_ru(this->ub, v.ub));
 8
 9
         }
10
         __device__ int sign() const {
11
12
              if (\text{this} \rightarrow \text{lb} > 0) // \text{lb} > 0 implies \text{ub} > 0
13
                   return 1;
              if (\text{this} \rightarrow \text{ub} < 0) // \text{ub} < 0 implies \text{lb} < 0
14
15
                   return -1;
              if (\text{this} \rightarrow \text{lb} = 0 \&\& \text{this} \rightarrow \text{ub} = 0)
16
17
                   return 0;
                  If none of the above conditions is satisfied, the sign of the
18
              11
              // exact result cannot be inferred from the interval, Thus, a flag
19
20
              // is returned to indicate an interval failure.
21
              return 2;
22
         }
23
          . . .
24
    private:
         double lb, ub; // Stores the interval's lower and upper bounds
25
26
    };
```

17

Implementation details, 3

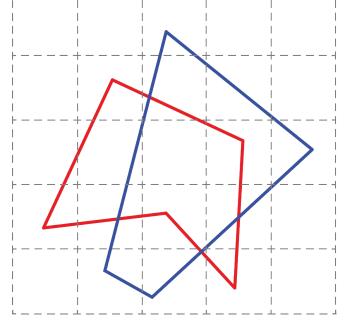
- Predicates: easily implemented using class instances
- Example: 2D orientation predicate

```
1
    struct CudaIntervalVertex {
          CudaInterval x, y;
 2
 3 \};
 4
 5
     __device__ int orientation (
          const CudaIntervalVertex* p,
 6
 7
          const CudaIntervalVertex* q,
 8
          const CudaIntervalVertex* r) {
          return ((q \rightarrow x - p \rightarrow x) * (r \rightarrow y - p \rightarrow y) -
 9
                     (q \rightarrow y - p \rightarrow y) * (r \rightarrow x - p \rightarrow x), sign();
10
11
```

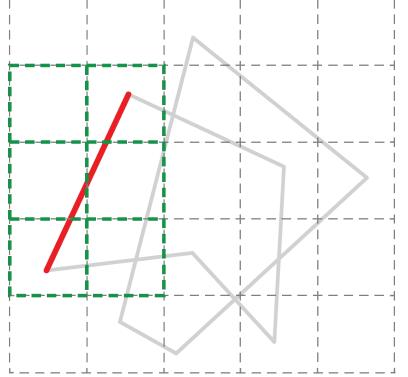
Fast red-blue intersection tests

- Case study: fast and exact algorithm for detecting red-blue intersection of line segments.
- Given two sets of segments S1 (red segments) and S2
 (blue segments) → find pairs of red-blue intersections.
- Possible quadratic number of red-red and blue-blue intersections, even though few red-blue intersections.
- So, harder than finding all segment intersections.
 - Sweep line is too inefficient here.
- Algorithm steps:
 - Uniform grid preprocessing filter on CPU identifies pairs of segments that may intersect
 - Interval analysis tests further filters those pairs on GPU,
 - Exact rational arithmetic back on CPU exactly tests a few pairs.

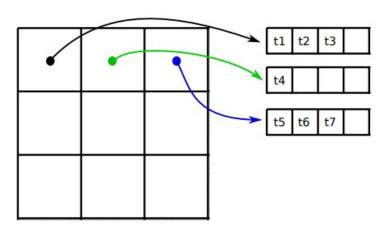
- Consider the following segment sets S1 (red) and S2 (blue):
- A uniform grid divides the domain into equally sized regions:

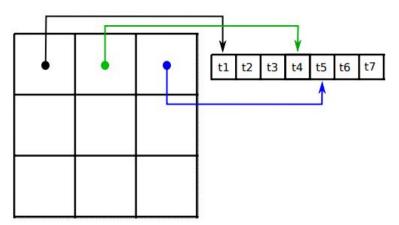


- Each segment (from both sets) is associated with the grid cells its bounding box intercepts.
- (Possible future mod would compute exactly which cells intersect the segment.)



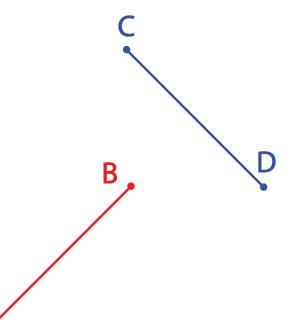
- For time and space efficiency, use a ragged array
 - One array containing all the elements, plus
 - Dope vector pointing to start of each cell's contents.
 - Constant time to read cell #i element #j.
- Creation requires two passes:
 - Count the number of elements in each cell, then
 - Insert the edges into the ragged array
- Both passes parallelize faster than dynamic sized arrays



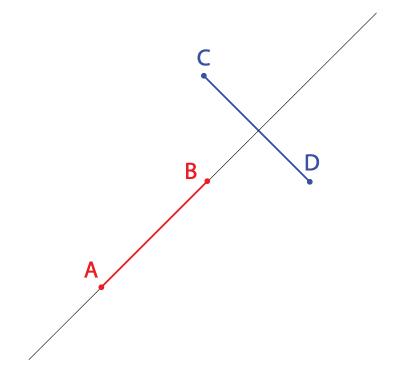


- Once the uniform grid is constructed, a list of the pairs of red and blue segments from all the grid cells is created
- This list is generated in parallel using a strategy similar to the creation of the ragged-array
 - first pass to perform the count of pairs of segments
 - second pass to insert the pairs into the list
- The list can than be sent to the GPU, which will evaluate which of those pairs do intersect.

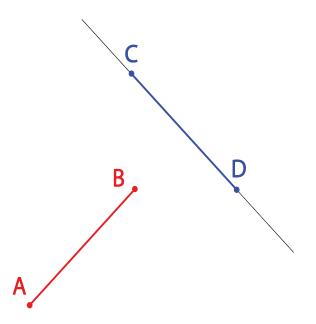
- Consider the segments AB and CD pictured below
- Four orientation predicates are sufficient to determine if they intersect or not
- intersect((A,B) , (C,D)) = orientation(A, B, C) ≠ orientation(A, B, D) ∧ orientation(C, D, A) ≠ orientation(C, D, B)



- C and D have different orientations w.r.t. (A,B)
- → CD intersects the supporting line of AB



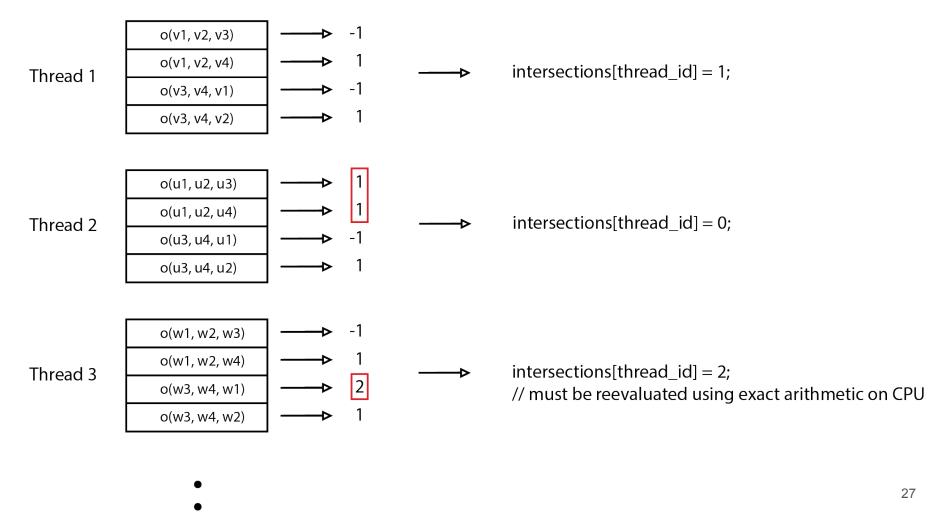
- A and B have the same orientation w.r.t. (C,D)
- → AB does not intersect the supporting line of CD



• A CPU implementation checks one pair of segments at a time, evaluating the four predicates in a for loop:

```
1
    . . .
 2
   for (int i = 0; i < n; ++i) {
        Vertex v1 = set_1 edges[i].v1;
 3
        Vertex v2 = set_1 edges[i].v2;
4
 5
       Vertex v3 = set_2 edges[i].v1;
        Vertex v4 = set_2 edges[i].v2;
6
 7
8
        int o1 = orientation (v1, v2, v3);
9
        int o2 = orientation(v1, v2, v4);
        int o3 = orientation(v3, v4, v1);
10
        int o4 = orientation(v3, v4, v2);
11
12
        intersections [i] = (o1 != o2) \&\& (o3 != o4);
13
14
   }
15
```

- List of pairs sent in one batch to GPU.
- One thread does one intersection test.



Experiments, 1

• Environment:

- AMD Ryzen 5 processor with 6 3.2GHz cores (12 hyperthreads)
- 16 GB of RAM
- NVIDIA GeForce GTX 1070 Ti GPU
- Arbitrary precision arithmetic provided by the GMP library
- OpenMP for parallelizing the CPU code
- Cuda for the GPU side
- Compared against CGAL:
 - Sequential method for detecting intersections of dD Iso-oriented Boxes (pre-processing)
 - Arithmetic filtering and lazy evaluation

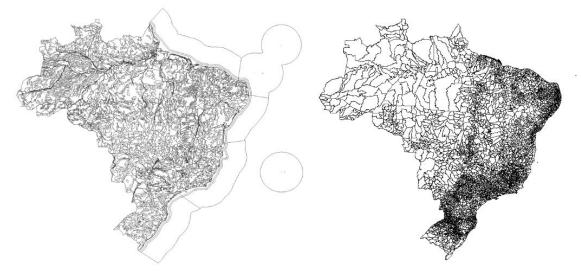
Experiments, 2

- Experiments have been performed using segments from four polygonal maps from two countries
- The intersection tests were made in pairs, using a 2500x2500 resolution uniform grid:
 - BrSoil x BrCounty
 - UsCounty x UsAquifers
 - UsCounty x UsCountyRotated
- Properties of each map:

			Pairs of	maps evaluate	ed	
	BrSoil	BrCounty	UsCounty	UsAquifers	UsCounty	UsCountyRot.
Number of segments	211,011	326, 193	3,740,989	352,924	3,740,989	3,740,989
Average segment length (% of bb.)	5×10^{-4}	4×10^{-4}	8×10^{-7}	1×10^{-4}	8×10^{-7}	8×10^{-7}
Percentage of empty grid cells		86%		98%		98%
Average # pairs of segments/cell		0.3		2.0		34.7
Number of pairs of segments		300,039		12,756,283		216, 542, 974
Number of intersections		20,860		11,948		11,751

Experiments, 3

BrSoil and BrCounty



UsCounty and Us Aquifers



Notes on the test data

- The edge segments are very unevenly distributed.
- Most grid cells are empty, a few have many edges.
- Yet the uniform grid works well.
- Quadtrees etc are not necessary (and are slower and don't parallelize well).
- Most intersection tests fail.
- That's ok because they're very fast and parallelize.

no filteri	ng	filtering, laz	5			vs. l	nterval*
		Cvaluation	BrCou	nty and BrS	Soil		
	Rationa	l* Interval*	CGAL*	Rational	Interval	GPU	Speedup
Pre.	1.24	12 0.225	0.478	0.549	0.324	0.099	2
Inter.	1.44	0.152	0.015	0.385	0.040	0.018	9
Total	2.68	86 0.377	0.493	0.934	0.364	0.117	3
# tests	300	K 300K	70K	300K	300K	300K	-
	0		UsCount	y and UsAq	uifers		
	Rationa	l* Interval*	$CGAL^*$	Rational	Interval	GPU	Speedup
Pre.	7.88	.84 0.812	2.628	1.610	0.392	0.164	5
Inter.	42.81	4.059	0.023	11.198	0.612	0.096	42
Total	50.70	00 4.871	2.651	12.808	1.004	0.260	19
# tests	131	M 13M	159K	13M	13M	13M	-
	-	U	JsCounty ar	nd UsCounty	yRotated		
	Rationa	l* Interval*	CGAL^*	Rational	Interval	GPU	Speedup
Pre.	14.53	32 1.422	7.482	2.798	0.454	0.251	6
Inter.	675.61	63.677	1.027	194.918	9.422	1.367	47
Total	690.14	48 65.099	8.509	197.716	9.876	1.618	40
# tests	2171	M 217M	11M	217M	217M	$217 \mathrm{M}$	-
				F	Parallel		

		BrCounty and BrSoil							
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	1.242	0.225	0.478	0.549	0.324	0.099	2		
Inter.	1.444	0.152	0.015	0.385	0.040	0.018	9		
Total	2.686	0.377	0.493	0.934	0.364	0.117	3		
# tests	300K	300K	70K	300K	300K	300K	-		
	9	UsCounty and UsAquifers							
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	7.884	0.812	2.628	1.610	0.392	0.164	5		
Inter.	42.816	4.059	0.023	11.198	0.612	0.096	42		
Total	50.700	4.871	2.651	12.808	1.004	0.260	19		
# tests	13M	13M	159K	13M	13M	13M	-		
		Us	sCounty an						
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	14.532	→ 1.422	7.482	2.798	0.454	0.251	6		
Inter.	675.616	63.677	1.027	194.918	9.422	1.367	47		
Total	690.148	65.099	8.509	197.716	9.876	1.618	40		
# tests	217M	217M	11M	$217 \mathrm{M}$	217M	217M	-		

CGAL: better pre-processing culling (but slower)

Interval*: faster culling and can be parallelized

Time not exactly proportional to number of tests (faster if pair does not intersect)

		BrCounty and BrSoil							
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	1.242	0.225	0.478	0.549	0.324	0.099	2		
Inter.	1.444	0.152	0.015	0.385	0.040	0.018	9		
Total	2.686	0.377	0.493	0.934	0.364	0.117	3		
# tests	300K	300K	70K	300K	300K	300K	-		
		UsCounty and UsAquifers							
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	7.884	0.812	2.628	1.610	0.392	0.164	5		
Inter.	42.816	4.059	0.023	11.198	0.612	0.096	42		
Total	50.700	4.871	2.651	12.808	1.004	0.260	19		
# tests	13M	13M	159K	13M	13M	13M	-		
		Us	sCounty an	d UsCounty					
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	14.532	1.422	7.482	2.798	0.454	0.251	6		
Inter.	675.616	63.677	1.027	194.918	9.422	1.367	47		
Total -	→ 690.148	65.099	8.509	197.716	9.876	1.618	40		
# tests	217M	217M	11M	217M	217M	$217 \mathrm{M}$	-		

Effect of arithmetic filtering Filters failed in only 0.000002% to 0.0005% of the predicates

 \rightarrow Rationals rarely necessary

 \rightarrow In the GPU implementation, CPU rarely had to re-evaluate with rationals.

		BrCounty and BrSoil							
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	1.242	0.225	0.478	0.549	0.324	0.099	2		
Inter.	1.444	0.152	0.015	0.385	0.040	0.018	9		
Total	2.686	0.377	0.493	0.934	0.364	0.117	3		
# tests	300K	300K	70K	300K	300K	300K	-		
-	7		UsCounty	and UsAq	uifers				
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	7.884	0.812	2.628	1.610	0.392	0.164	5		
Inter.	42.816	4.059	0.023	11.198	0.612	0.096	42		
Total	50.700	4.871	2.651	12.808	1.004	0.260	19		
# tests	13M	13M	159K	13M	13M	13M	-		
	-	Us	sCounty an	d UsCounty	Rotated				
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	14.532	1.422	7.482	2.798	▶ 0.454	0.251	6		
Inter.	675.616	63.677	1.027	194.918	9.422	1.367	47		
Total	690.148	65.099	8.509	197.716	9.876	1.618	40		
# tests	217M	$217 \mathrm{M}$	11M	217M	217M	217M	1		

Pre-processing: not entirely on the CPU -- GPU computes in which cell each vertex is. (this is not a predicate, but can be computed with IA and filtering)

GPU pre-processing: includes copying intervals (coordinates) to the GPU.

		BrCounty and BrSoil							
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	1.242	0.225	0.478	0.549	0.324	0.099	2		
Inter.	1.444	0.152	0.015	0.385	0.040	0.018	9		
Total	2.686	0.377	0.493	0.934	0.364	0.117	3		
# tests	300K	300K	70K	300K	300K	300K	-		
-	7		UsCounty	and UsAq	uifers				
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	7.884	0.812	2.628	1.610	0.392	0.164	5		
Inter.	42.816	4.059	0.023	11.198	0.612	0.096	42		
Total	50.700	4.871	2.651	12.808	1.004	0.260	19		
# tests	13M	13M	159K	13M	13M	13M			
		Us	sCounty an	d UsCounty	Rotated				
	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup		
Pre.	14.532	1.422	7.482	2.798	0.454	0.251	6		
Inter.	675.616	▶ 63.677	1.027	194.918	9.422	►1.367	-47		
Total	690.148	65.099	8.509	197.716	9.876	1.618	40		
# tests	217M	217M	11M	217M	217M	217M	-		

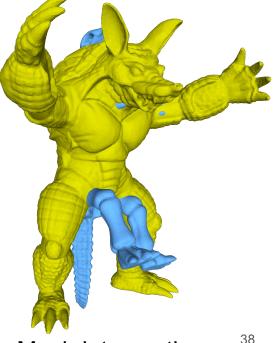
Inter.: 0.685s prepare + 62.992s eval. (prep. = generate (parallel) list of edges to test) GPU : 1.149s prepare + 0.218s eval. (prep. = same as CPU + copy ids to/from GPU) $_{36}$ 289x speedup in evaluation \rightarrow Possibly better speedups in algs. w/less communication

Conclusions and future work, 1

- Good for interactive applications (CAD, GIS, CG, ...)
- Intervals do not fail often (fail \rightarrow re-evaluation)
- More efficient to keep data on the GPU and re-use
 - If coordinates will be re-used, copy to the GPU at the beginning of the program.
 - Use communication only for what is really necessary.
 (e.g.: for intersections, copy the ids of the pairs of the edges)
 - E.g.: boolean operations: detecting intersection is only one step → data can be kept on the GPU and re-used in all steps.

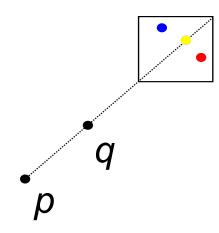
Conclusions and future work, 2

- Future work:
 - Apply to 2D/3D point location, mesh intersection and other CG algorithms.
 - Improve the performance of the predicates.
 - E.g.: reduce CPU-GPU communication overhead (move combinatorial part of the algorithm to GPU, overlap communication/processing, etc).
- Challenges:
 - Predicates must be evaluated in batch
 - Have to "manually" keep track of how each interval was generated (ok mainly when depth of the computation tree is small)
 - Intervals may fail more often in applications with deep computation trees.

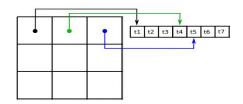


Mesh intersection

Thank you!



	/		t1 t2 t3
1	•		t4
			t5 t6 t7
-			





			BrCou	nty and BrS	Soil		
a	Rational*	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup
Pre.	1.242	0.225	0.478	0.549	0.324	0.099	2
Inter.	1.444	0.152	0.015	0.385	0.040	0.018	9
Total	2.686	0.377	0.493	0.934	0.364	0.117	3
# tests	300K	300K	70K	300K	300K	300K	-
2			UsCounty	v and UsAq	uifers		
	Rational*	Interval*	$CGAL^*$	Rational	Interval	GPU	Speedup
Pre.	7.884	0.812	2.628	1.610	0.392	0.164	5
Inter.	42.816	4.059	0.023	11.198	0.612	0.096	42
Total	50.700	4.871	2.651	12.808	1.004	0.260	19
# tests	13M	13M	159K	13M	13M	13M	
2		U	sCounty an	d UsCounty	yRotated		
	$Rational^*$	$Interval^*$	CGAL^*	Rational	Interval	GPU	Speedup
Pre.	14.532	1.422	7.482	2.798	0.454	0.251	6
Inter.	675.616	63.677	1.027	194.918	9.422	1.367	47
Total	690.148	65.099	8.509	197.716	9.876	1.618	40
# tests	217M	217M	11M	217M	217M	217M	-
	o(v1, v2, v3		-1				
Thread 1	o(v1, v2, v4		1	→ interse	ctions[thread_	_id] = 1;	
	o(v3, v4, v1)>	-1				
	o(v3, v4, v2		1				
	o(u1, u2, u3	3	1				
	o(u1, u2, u4		1				
Thread 2	o(u3, u4, u		-1	→ interse	ctions[thread_	_id] = 0;	
	o(u3, u4, u2		1				
	0(03, 04, 02	2)					
	o(w1, w2, v	v3)	-1				
	o(w1, w2, v		1			:41 0	
Thread 3	o(w3, w4, v	v1)	2		ctions[thread_ t be reevaluate		act arithmetic
	o(w3, w4, v		1	// musi	L DE IEE Valuale	to using ex	
]					

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